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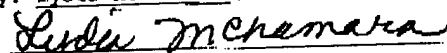
JAN 26 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

Date : 1/26/2006
IN RE APPLICATION OF: Alan R. Ball et al.
APPLN. NO. : 10/813501
FILED : 3/31/2004
FOR : METHOD OF FORMING A SELF-GATED
TRANSISTOR AND STRUCTURE THEREFOR
EXAMINER : Quan Tra
Phone : 571-272-1755
Group : 2816

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING FACSIMILE
TRANSMITTED TO THE PATENT AND TRADEMARK OFFICE,
ATTN EXAMINER Quan Tra
AT THE FAX NO. 571-273-8300
ON: 1/26/2006 (Date of Facsimile Transmission)

by: Lydia McNamara (Name of Person Signing Certificate)


SIGNATURE1/26/2006
DATENOTICE OF APPEAL TO THE
BOARD OF PATENT APPEALS AND INTERFERENCES

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

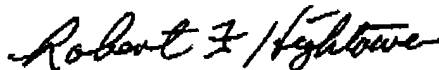
SIR:

In accordance with 37 CFR 41.31, applicants hereby appeal to the
Board of Patent Appeals and Interferences from the final rejection by
the Examiner of claims 1-20.

Please charge the cost of the Appeal (\$500.00) to Deposit Account No.
50-1086.

Respectfully submitted,
Alan R. Ball et al.

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